

FIG. 1

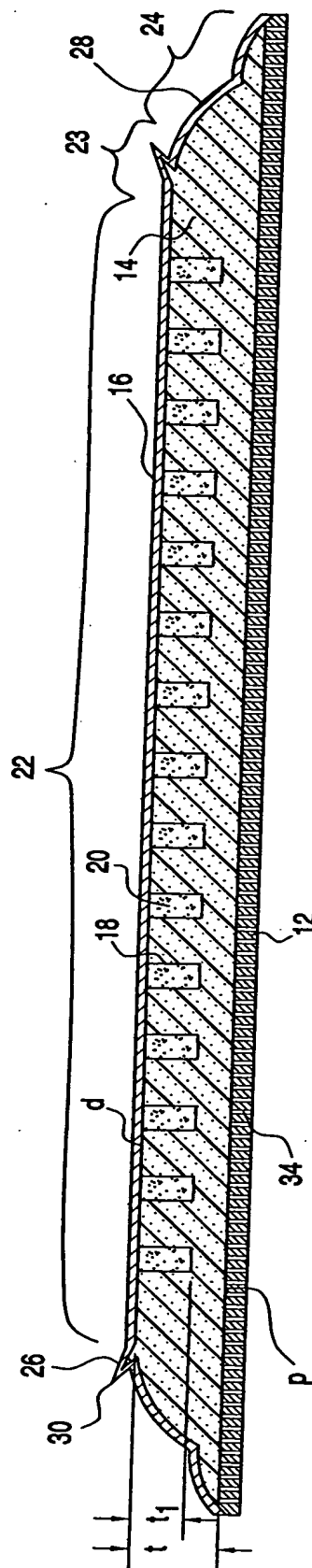


FIG. 2

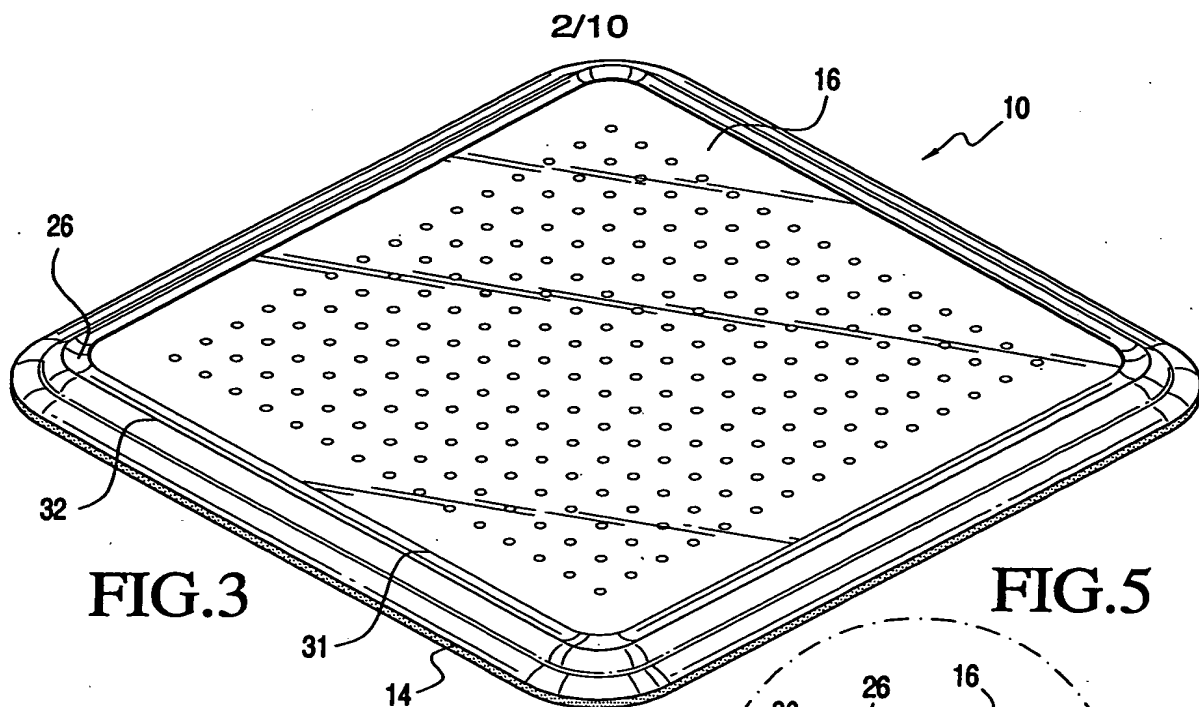


FIG. 3

FIG. 5

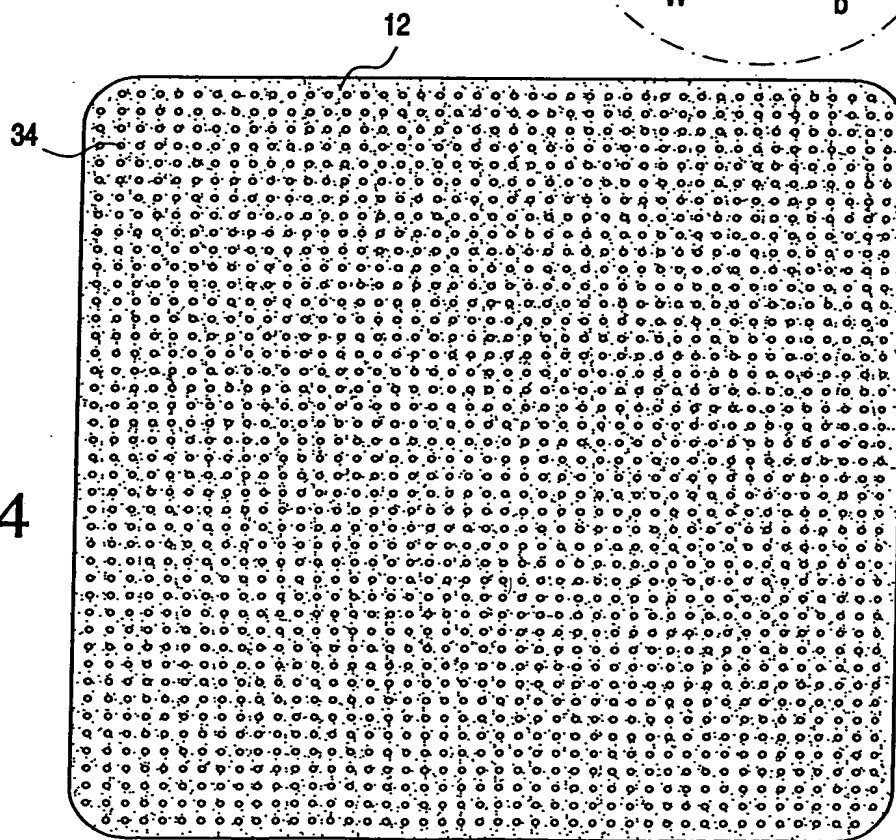
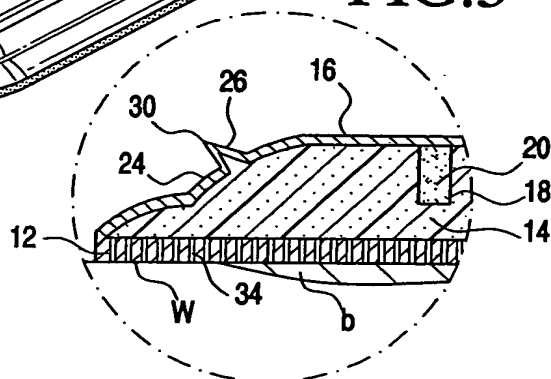


FIG. 4

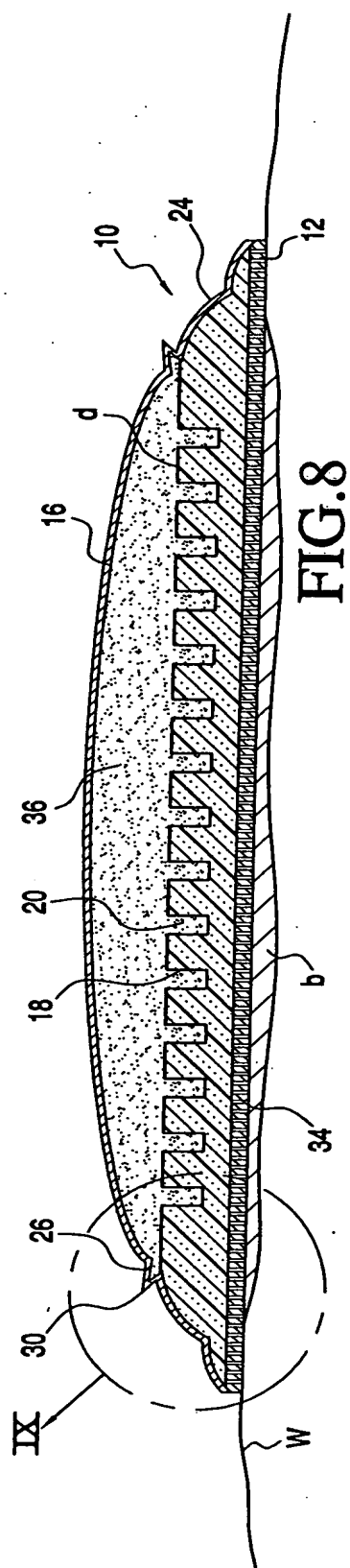
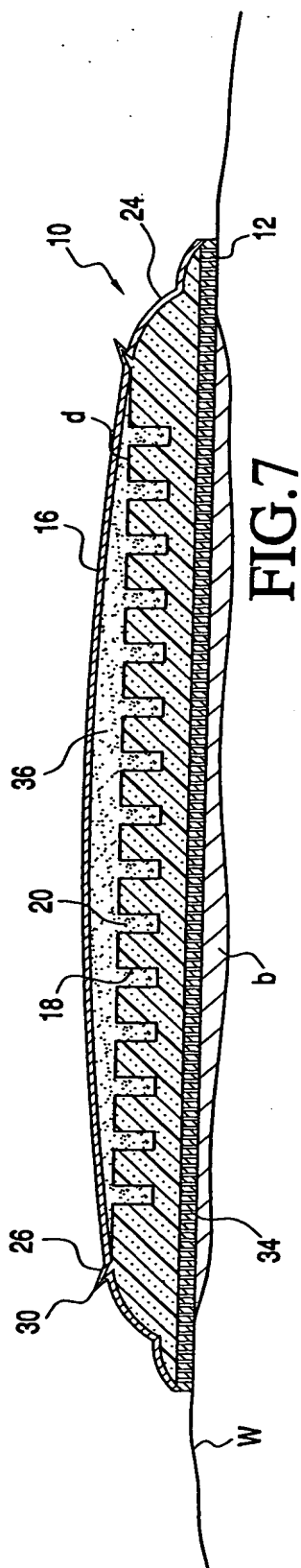


FIG. 1 is a schematic diagram of a square-shaped substrate 12 with rounded corners. The substrate has a central square region 34 filled with a dense grid of small dots. The outer region of the substrate is labeled 19.

FIG. 1 is a schematic diagram of a square-shaped device 10. The device has a central region 14 containing a grid of small circles. This central region is surrounded by a border 18, which is further enclosed by an outer frame 22. A diagonal line 24 is drawn across the central region 14.

FIG. 1 is a perspective view of a rectangular substrate 12 with rounded corners. The surface of the substrate is covered with a dense array of small, circular features 34. A bracket 86 indicates a portion of this array.

FIG.13

FIG.14

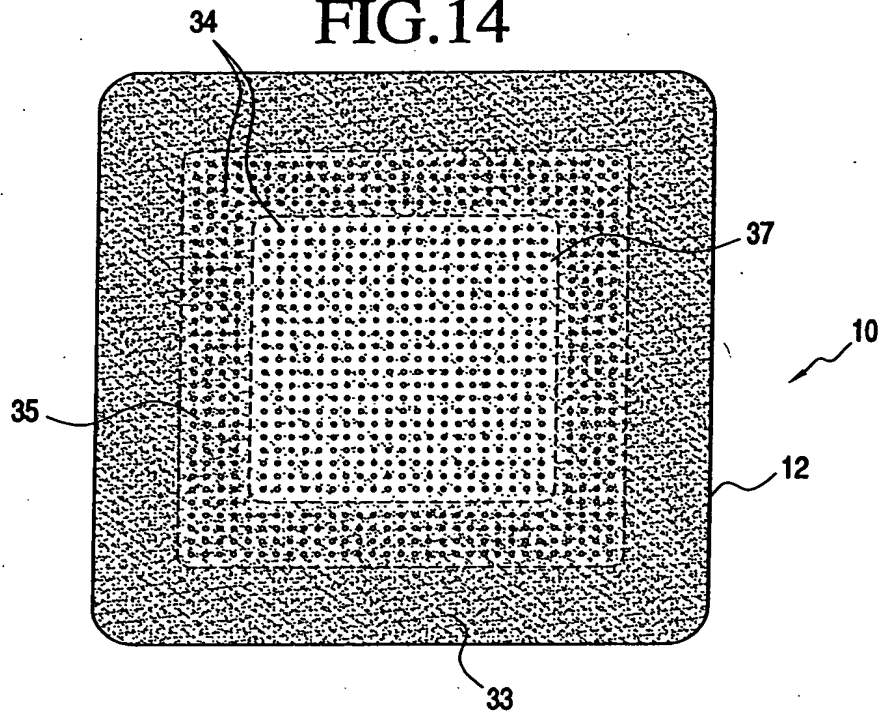


FIG.15

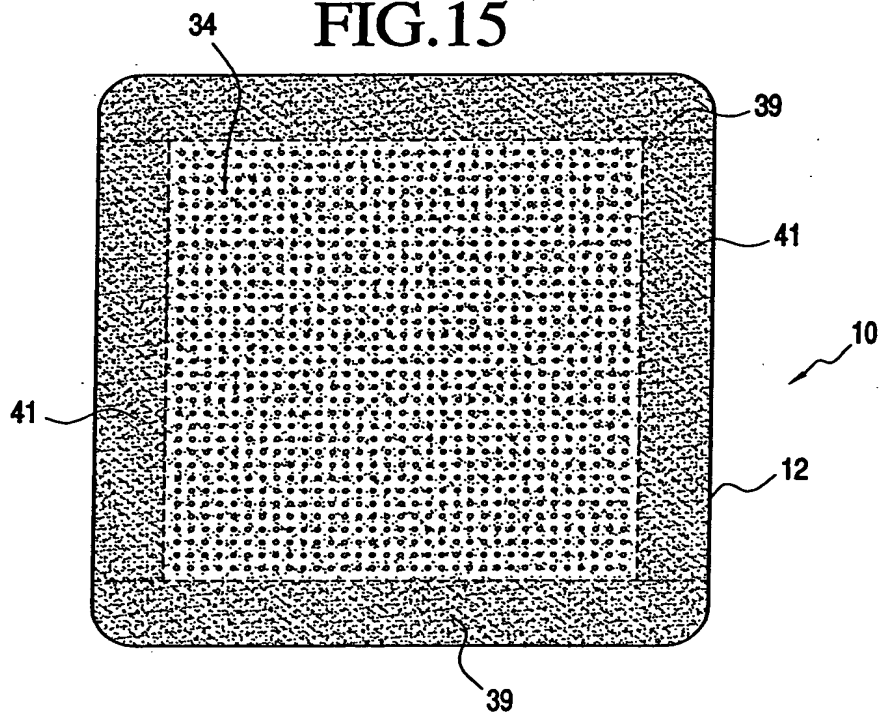


FIG.16

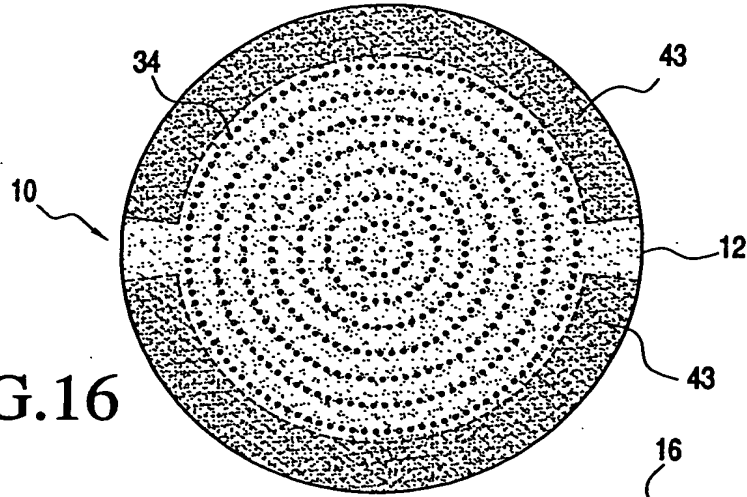


FIG.17

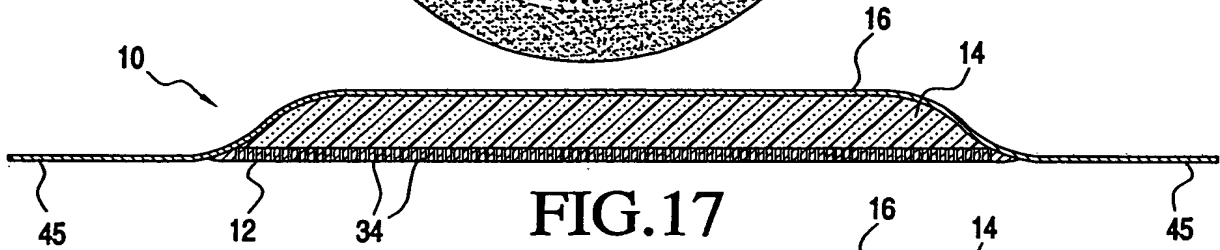


FIG.18



FIG.19

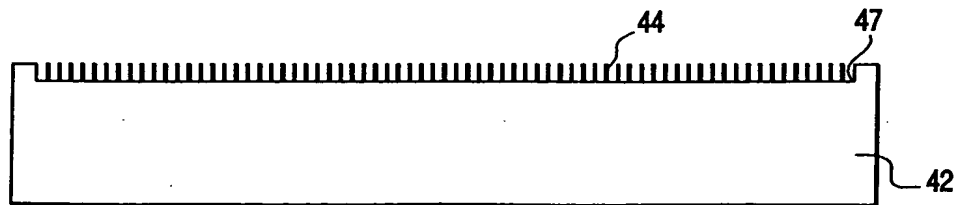
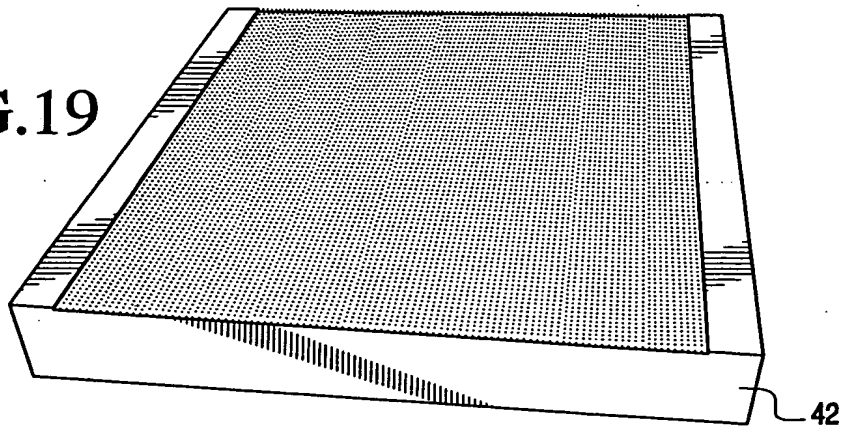


FIG.20

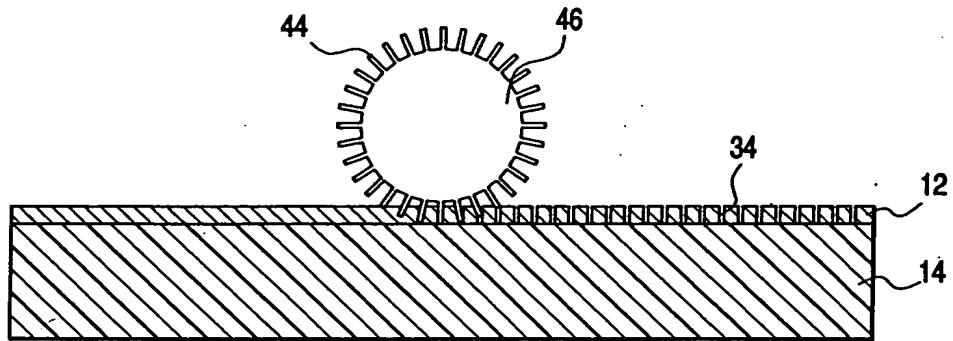


FIG. 21

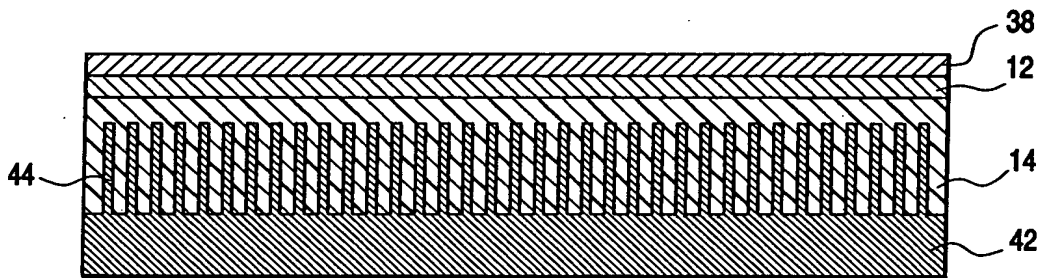


FIG. 22

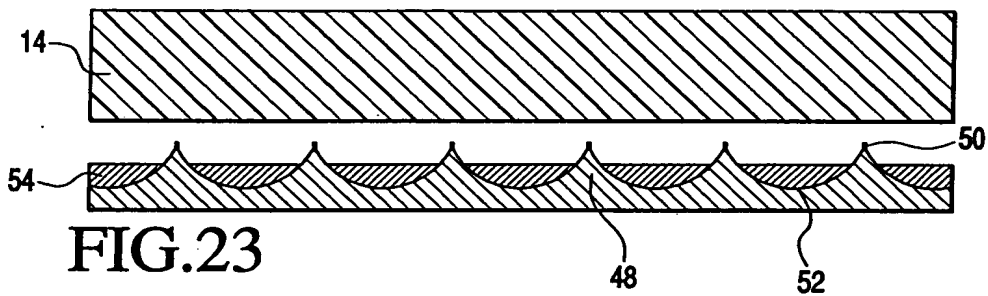


FIG. 23

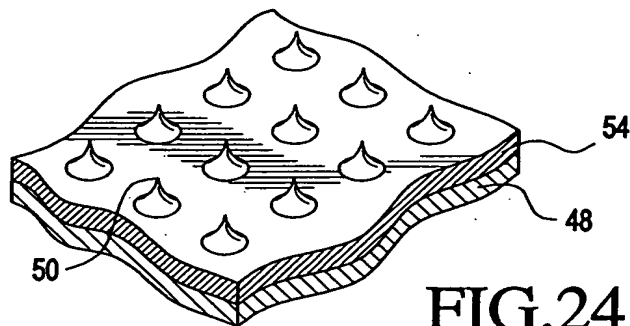
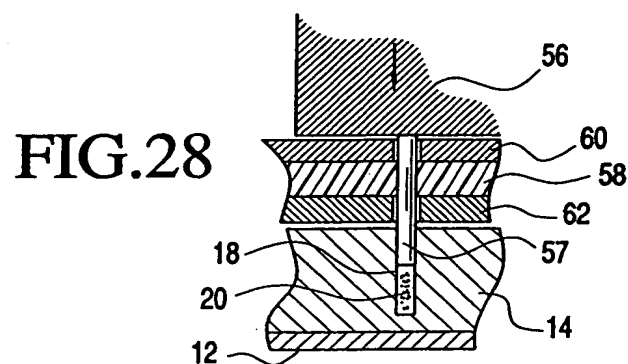
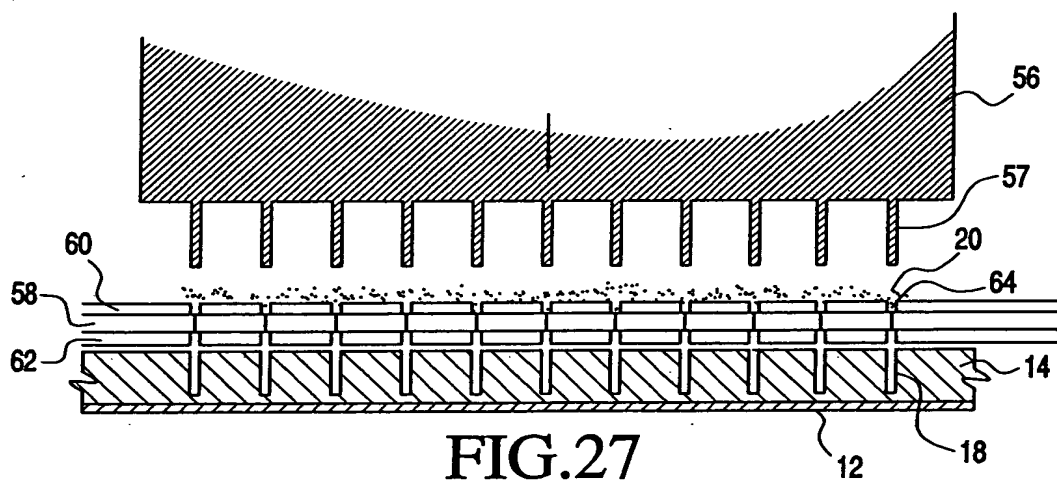
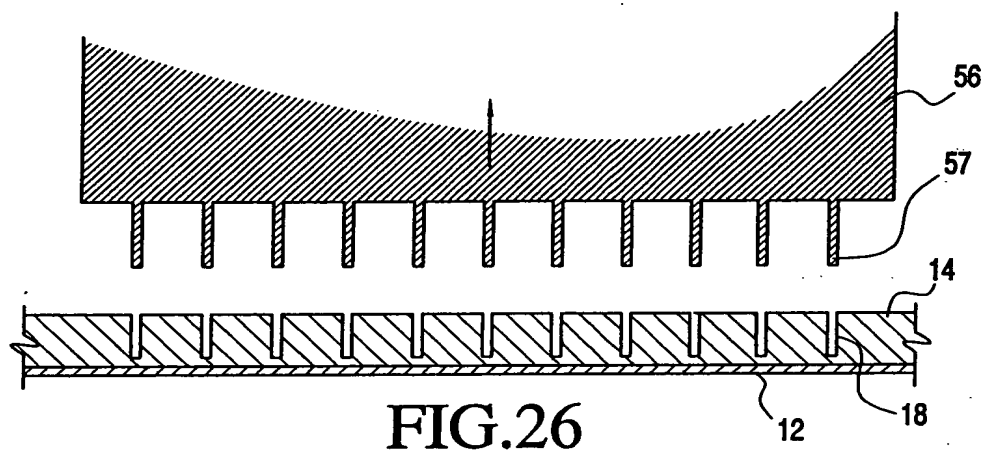
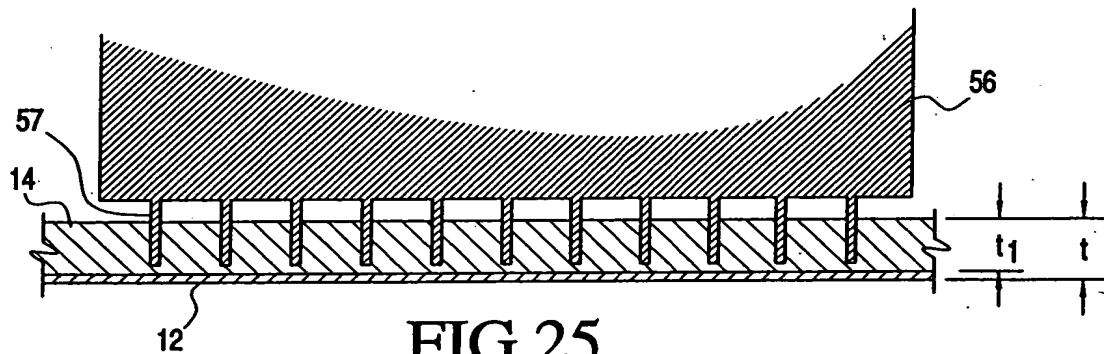


FIG. 24





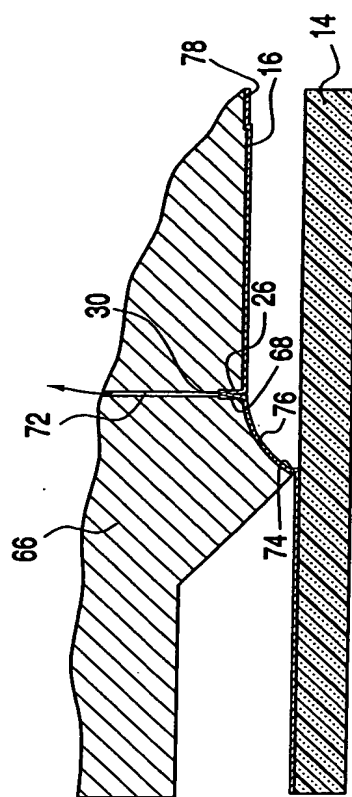
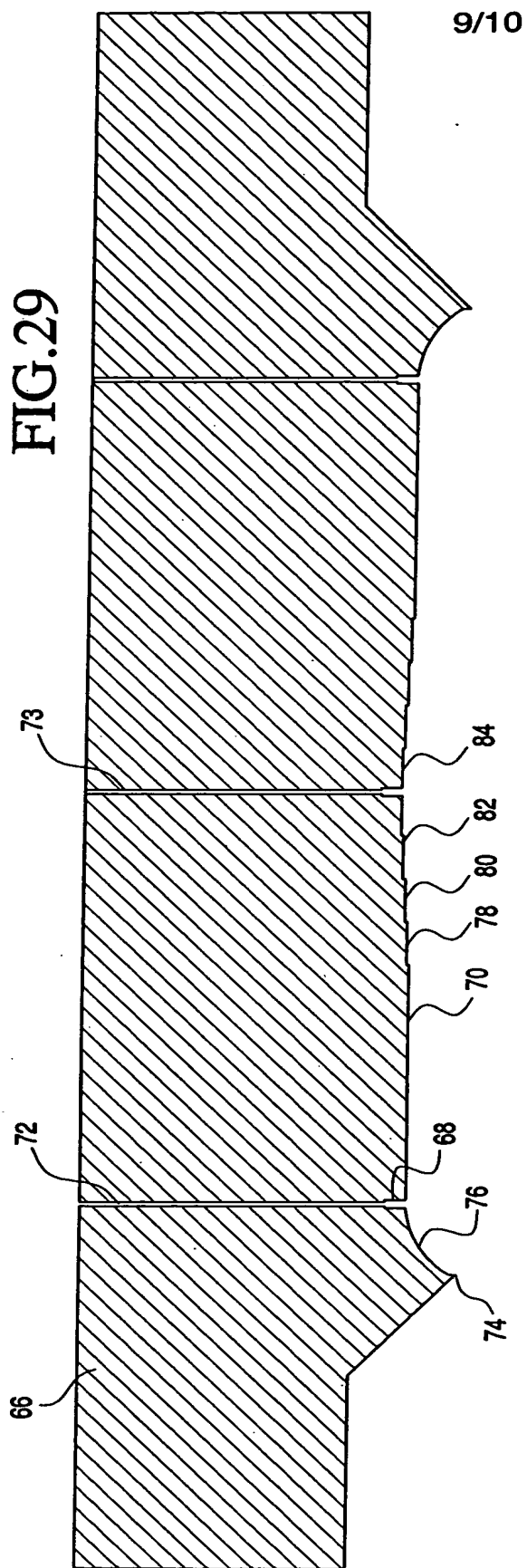


FIG.31

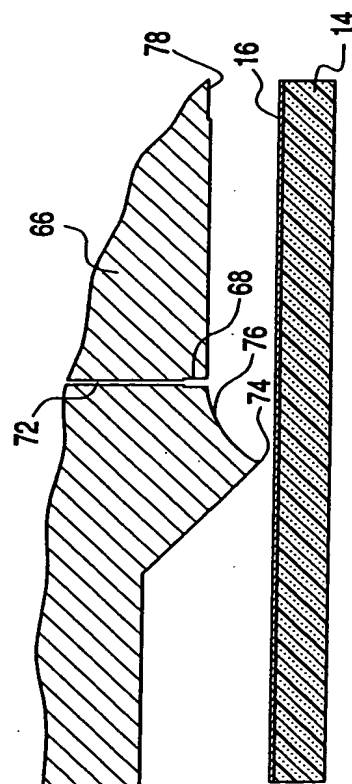


FIG.30

